



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 3480

| | | | | |
|-----------------------------|---------------------------------------|--------------|------------------------|------------------------------------|
| SERIAL NUMBER 09/856,823 | FILING DATE 05/25/2001 RULE | CLASS 117 | GROUP ART UNIT 1722 | ATTORNEY DOCKET NO. 0819-559 |
|-----------------------------|---------------------------------------|--------------|------------------------|------------------------------------|

APPLICANTS

Shin Hashimoto, Osaka, JAPAN;
 Takenobu Kishida, Kyoto, JAPAN;
 Kyoko Egashira, Kyoto, JAPAN; Yoshifumi Hata, Kyoto, JAPAN;
 Toru Nishiwaki, Toyama, JAPAN;
 Tomoya Tanaka, Kyoto, JAPAN;

** CONTINUING DATA *****
 This application is a 371 of PCT/JP00/06851 10/03/2000 M)

** FOREIGN APPLICATIONS *****
 JAPAN 11-282668 10/04/1999 M)
 JAPAN 2000-263467 08/31/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 11/08/2002

| | | | | |
|---|------------------------------|------------------------|-----------------------|----------------------------|
| Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | STATE OR COUNTRY JAPAN | SHEETS DRAWING 8 | TOTAL CLAIMS 16 | INDEPENDENT CLAIMS 2 |
| 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | | | | |
| Verified and Acknowledged <i>[Signature]</i> M) | Examiner's Signature | Initials | | |

ADDRESS
 Nixon Peabody
 Suite 800
 8180 Greensboro Drive
 McLean, VA
 22102

TITLE
 Method for fabricating semiconductor device

☐ All Fees